Application Note-14

WRITE COMMAND ISSUE OF 4M LPC (W39V040A)

1. GENERAL DESCRIPTION

The Winbond LPC (Low Pin Count) Flash, W39V040A, is an application-specific device, which is used for non-Intel Chipset solution.

This application note is to propose a simple way to recover chip operation if system user accidentally issues a Write Command at un-coded address.

2. DETAIL DESCRIPTION

The decoded address range of W39V040A is mapped to the top of 4GB memory space from "FFFF-FFFF to FF00-0000." Any Write Command issued at an un-decoded address will trigger the device into Write mode and therefore causes the device cannot normally be read. Only when a Write Command issued at decoded address, the device can be normally read again. In other words, the Read function will be disturbed after the Write Command issued at any un-decoded address.

If a Write Command is issued at an un-decoded address, an Exit Command should be issued so that the chip will resume normal function.

The exit command is:

ADDRESS	DATA
FFFF-XXXX	F0

3. CONCULSION

In using Winbond 4M LPC, W39V040A, we strongly recommend user to follow up the Write Command rule to keep off any unexpected failure.